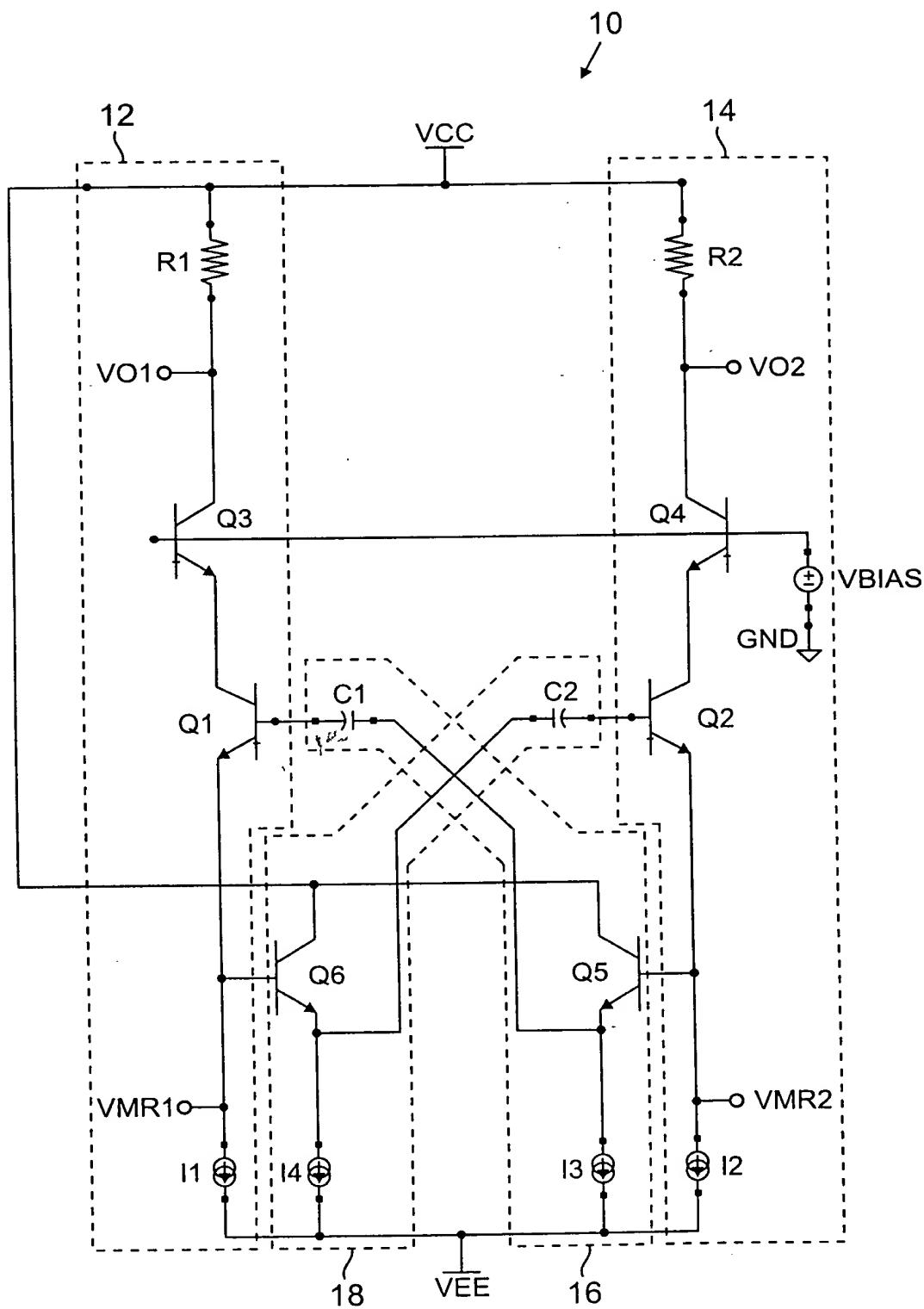
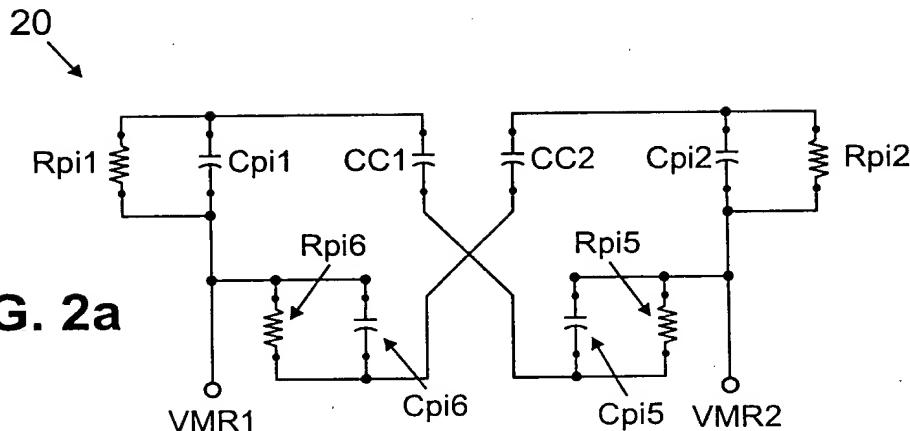


65 62 60 58 56 54 52 50 48 46 44 42 40 38 36 34 32 30 28 26 24 22 20

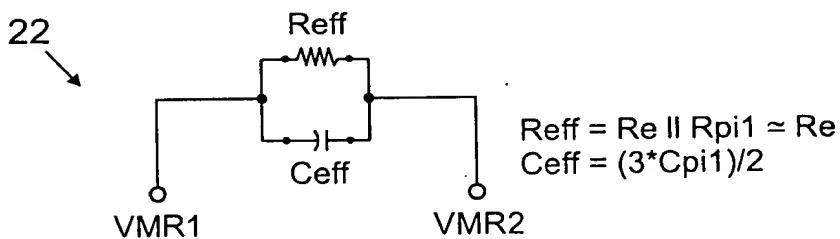
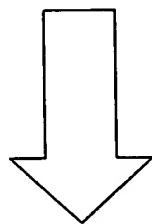
**FIG. 1**



**FIG. 2a**

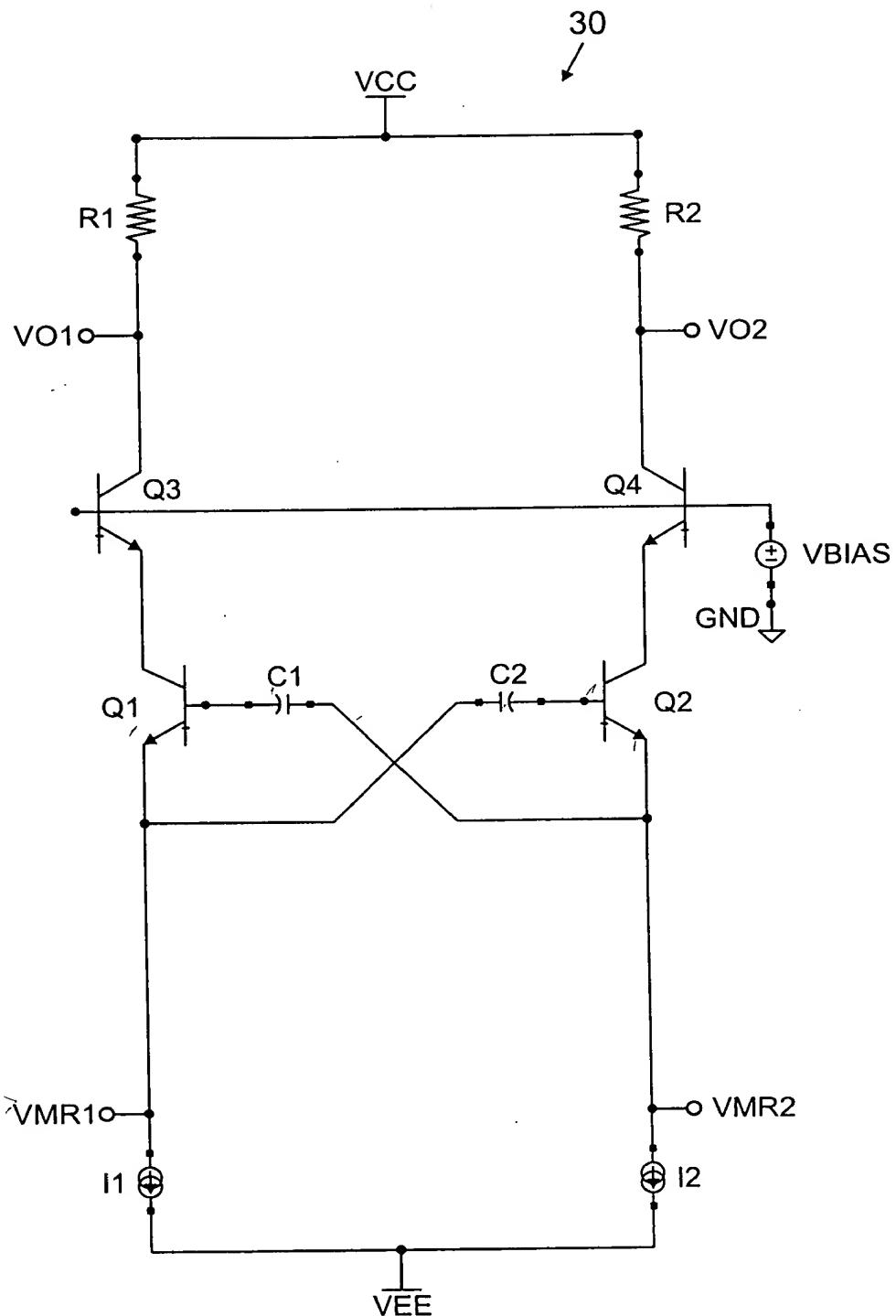
## EQUIVALENT INPUT CIRCUIT

CC1 >> Cpi1 & CC1 >> Cpi2  
CC2 >> Cpi1 & CC2 >> Cpi2  
CC1 >> Cpi5 & CC1 >> Cpi6  
CC2 >> Cpi5 & CC2 >> Cpi6

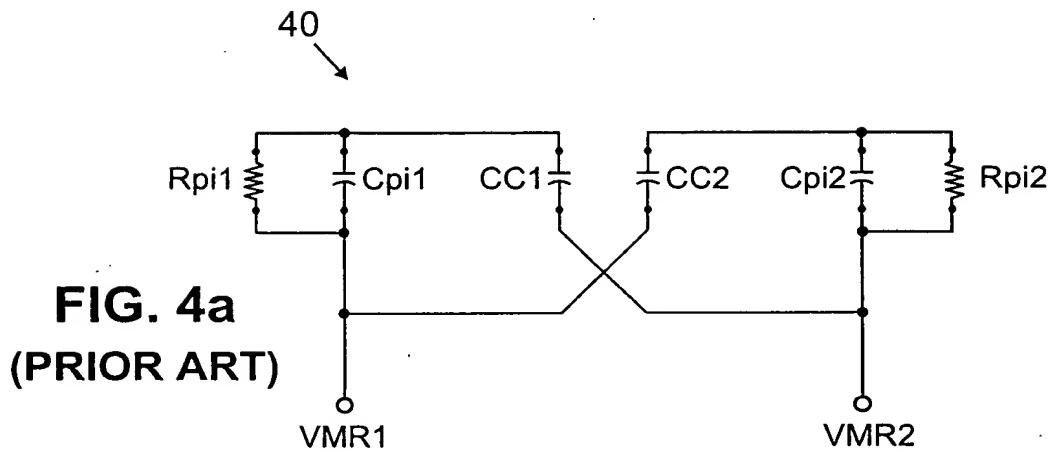


## SIMPLIFIED CIRCUIT

**FIG. 2b**

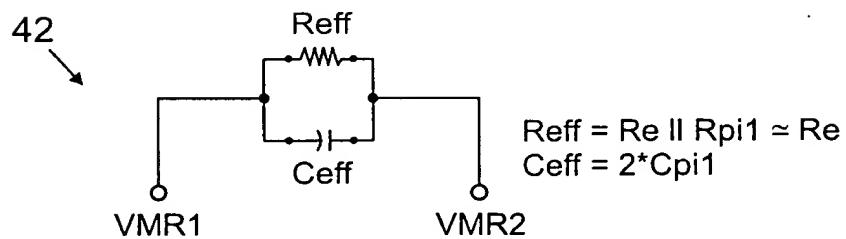
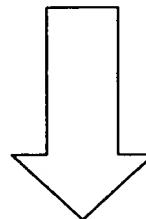


**FIG. 3**  
**(PRIOR ART)**



EQUIVALENT INPUT CIRCUIT

$$\begin{aligned}CC1 &>> Cpi1 \text{ & } CC1 >> Cpi2 \\CC2 &>> Cpi1 \text{ & } CC2 >> Cpi2\end{aligned}$$



**FIG. 4b  
(PRIOR ART)**

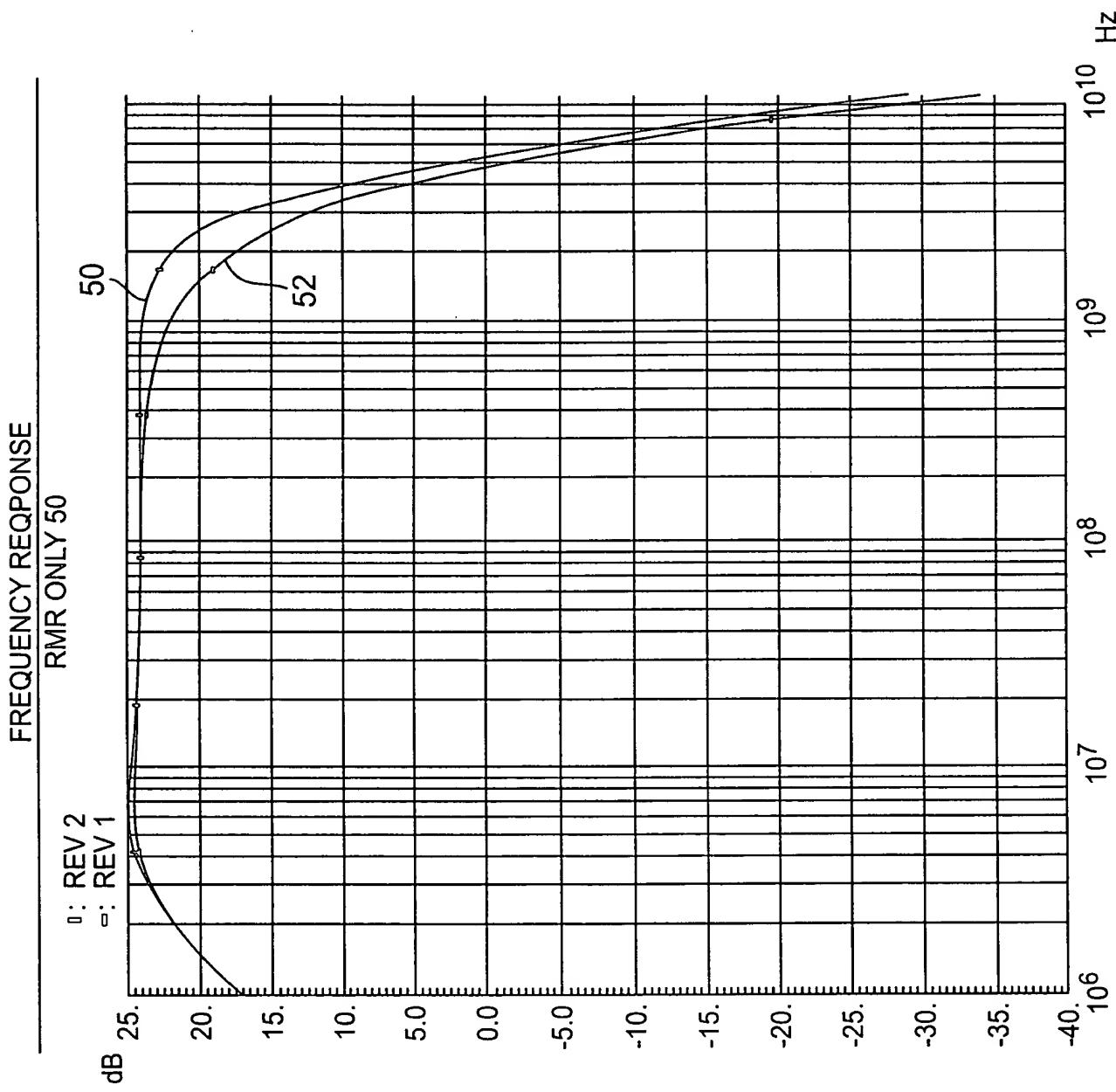


FIG. 5

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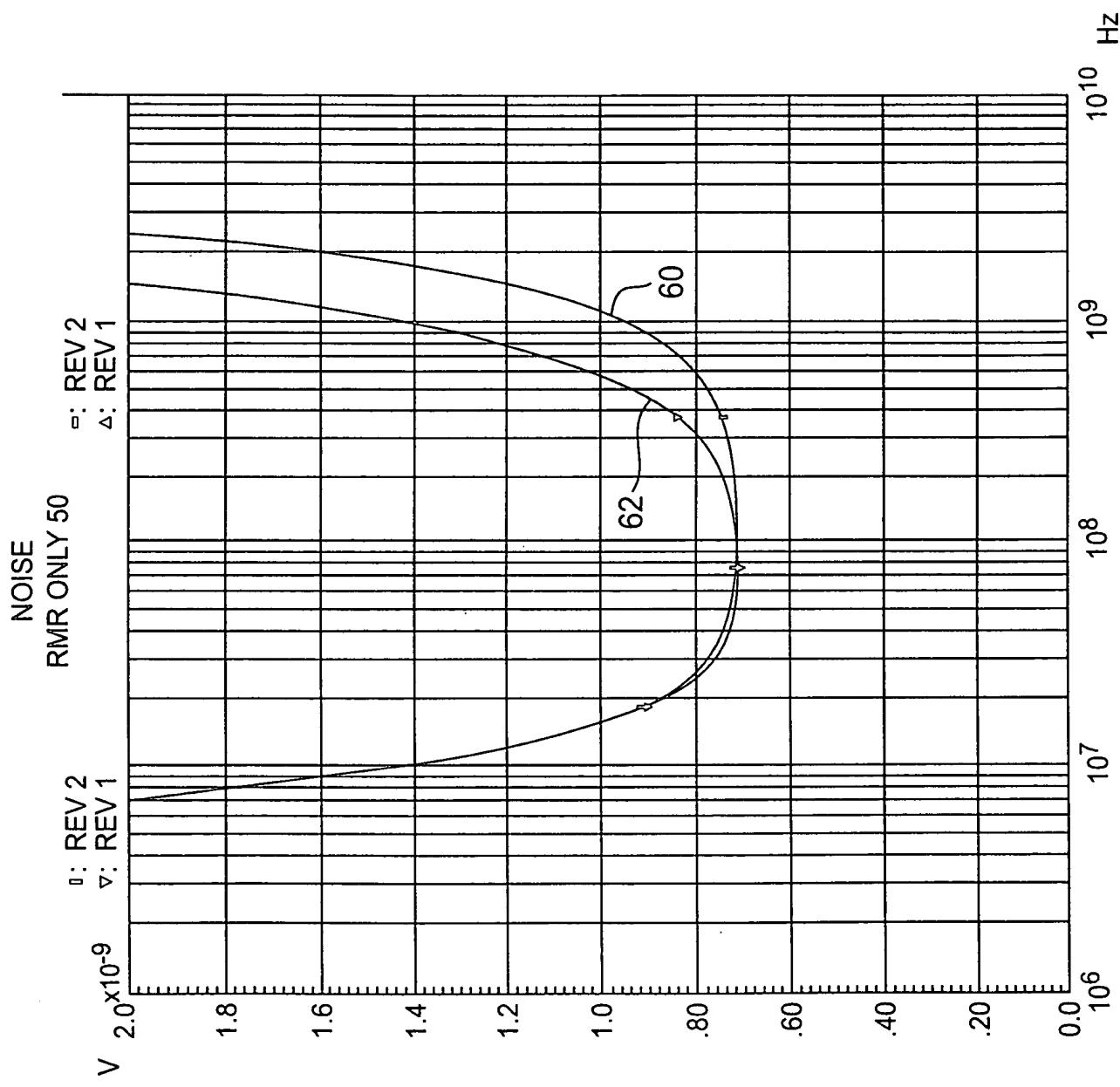


FIG. 6